Attorney Docket No. 02-IND-015 Client Matter No. 19204.0004

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial. No.

Inventor:

Rajesh NARWAL

Filed:

September 29, 2003

TC/A.U.

Examiner:

Docket No.

02-IND-015

Customer No.

25235

Confirmation No.

Title: VOLTAGE LEVEL TRANSLATOR FOR TRANSLATING LOW TO HIGH VOLTAGE LEVELS IN DIGITAL INTEGRATED

CIRCUITS

## INFORMATION DISCLOSURE STATEMENT

**Commissioner for Patents** Alexandria, VA 22313-1450

Sir:

Applicant hereby submits for filing under 37 CFR 1.97 (b) a disclosure statement. The patents, publications or other information of which Applicant is presently aware are listed in Form PTO/SB/08A submitted herewith. In submitting this reference, no representation is made or implied that the reference is or is not material to the examination of this application. Pursuant to 37 CFR 1.491(b), a copy of the reference is not provided.

No fee is believed due. However, any fee deficiency associated with this submittal may be charged to Deposit Account No. 50-1123.

Respectfully submitted,

September 29, 2003

Carol W. Burton, Reg. No. 35,465

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PTO/SB/08A ( (Substitute fo		1449A/PTO)		ATTY. DOCKET NO. 02-IND-015	APPLICATION NO			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				FIRST NAMED INVENTOR: Rajesh NARWAL				
(Use several sheets if necessary) Sheet 1 of 1				FILING DATE: September 29, 2003 ART UNIT				
			U.S. PATENT	F DOCUMENTS				
Examiner Initials	Cite No.	Document No. No. – Kind Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Doc	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
		5,422,523	06-06-1995	ROBERTS ET AL.				
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